

NANOSCALE GRAPHENE LITHOGRAPHY USING AN ATOMIC FORCE MICROSCOPE

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ABSTRACT

In this work, we have systematically studied local oxidation lithography parameters using an Atomic Force Microscope (AFM) on graphene material. Graphene has recently been shown to have exceptional electrical properties which give it a niche in emerging nanoelectronics applications requiring quantum structures. The desktop AFM nanolithography technique has lately been shown [1-3] to fabricate graphitic nanodevices on the order of tens of nanometers. By applying an appropriate electric field between AFM tip and substrate in humid atmosphere, oxidation of the substrate occurs. Depending on such process parameters as applied voltage, tip speed, water meniscus length and humidity, the oxidation of the graphitic material enables the formation of insulating trenches to make various nanostructures. Using this optimized technique, we have oxidized nanometer-sized features on single and few layer graphene and graphite.

Keywords: Atomic Force Microscope, Graphene, Graphite, Lithography

1 INTRODUCTION

The Atomic Force Microscope (AFM) can be used to locally oxidize various materials to form nanometer size features [4-12]. Recently, the AFM has been used to etch graphene using the principle of local anodic oxidation. Application of an appropriate electric field between the tip and substrate dissociates the H₂O molecules into H⁺ and OH⁻. The H⁺ ions rush towards the negatively charged tip and the OH⁻ ions gather near the positively substrate. The oxygen reacts with the carbon in the graphitic material to form volatile or nonvolatile carbon oxides depending on the voltage applied. This oxidation, coupled with the x-y scanning capability of the AFM, allows for thin structure patterning ability. However, the effect of process parameters on feature size has not been completely studied. If the lithography parameters such as voltage, tip speed, meniscus length and humidity are well controlled, sub-100 nm patterning is possible on graphitic materials, especially for emerging nanoelectronics applications requiring quantum structures [13-18]. This technique can also be performed in the ambient environment, eliminating several fabrication steps, such as the poly(methyl methacrylate) (PMMA) processing required in conventional electron-beam lithography process. In order to establish this

technique as a robust fabrication process, we have studied the process parameters and resulting feature size.

2 EXPERIMENTAL PROCEDURES

Graphite and few-layer graphene were mechanically exfoliated on silicon substrate. The samples were connected electrically to ground and placed in Pacific Nanotechnology NANO-I AFM system (Figure 1). Imaging was conducted in contact mode with a negatively biased sharp AFM tip. Patterns were drawn by the tip in this configuration with applied tip voltage running between -5V and -10V.

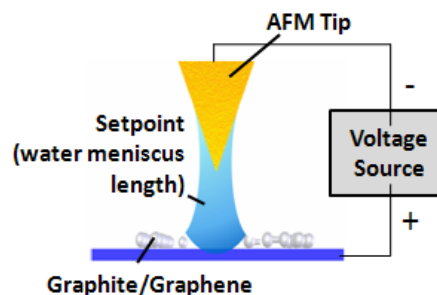


Figure 1: Schematic diagram of AFM oxidation lithography setup. An external voltage source is used to ground the graphite substrate and negatively bias the AFM tip. The water meniscus which naturally condenses around the tip and graphite facilitates local oxidation of the graphite upon application of the electric field.

Line patterns were created by scanning the AFM tip in a line across the substrate at a constant speed and setpoint while continuously applying a constant voltage. For parametric experiments, the water meniscus length (known as setpoint) was varied from 0.00 to 130 nm, and tip speed from 0.03 to 0.10 $\mu\text{m/s}$. Dot patterns were created by holding the tip steady above a point on the substrate and applying voltage for a specified period of time (holdtime). Holdtime, i.e. oxidation time per pixel is directly related to the tip speed, where one pixel is equal to the diameter of the AFM tip. The holdtime was varied from 250 ms to 500 ms. The resulting depth and width (diameter) of the lines (and dots) were measured and analyzed using the Pacific Nanotechnology Nanorule software.

3 RESULTS

Figures 2 shows AFM scans confirming the relationship between feature size and setpoint, voltage, and tip speed (or

holdtime). In Figure 2a, as the tip is raised above the substrate from 35.5 nm to 59.2 nm in increments of 11.8 nm, the expected reduction in feature size is observed. Similarly, in Figure 2c, as the tip speed is varied from 0.03 $\mu\text{m/s}$ to 0.10 $\mu\text{m/s}$ in increments of 0.04 $\mu\text{m/s}$, feature size is observed to decrease.

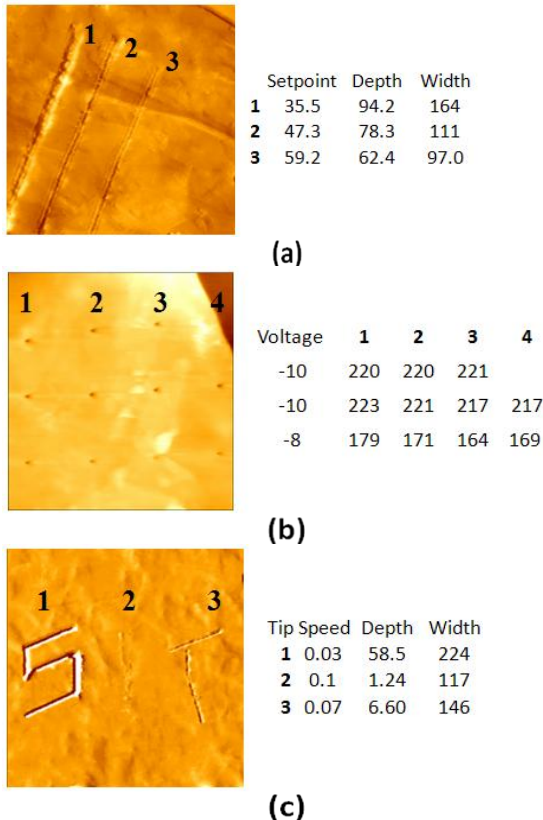


Figure 2: (a) Line patterns showing variation of feature size with setpoint, (b) Dot pattern showing variation of feature size with voltage, (c) Line patterns showing variation of feature size with tip speed. AFM images of the local oxidation lithography process. Resulting feature sizes are shown in the tables next to each AFM scan. Experimental conditions: (a) voltage – -9V, tip speed – 0.03 $\mu\text{m/s}$, humidity - <20%, (b) setpoint – 0 nm, holdtime – 500 ms (0.06 $\mu\text{m/s}$), humidity - <20%, (c) voltage – -10V, setpoint – 0 nm, humidity - <20%.

Figure 2b shows dot patterns which illustrate the relationship between feature size and voltage. The expected trend of feature size (width) increasing with decreasing (negative) voltage is shown to hold. We find that on average, for the same voltage and setpoint and tip speed (holdtime), the dot patterns consistently produce larger feature sizes than the line patterns. We attribute this to the stability of the water meniscus during dot patterns. In the line scan, the tip is scanned across the hydrophobic graphite which can contribute to thinner line widths. However, in the dot patterns, the tip remains stationary above the graphite, which allows the meniscus to remain stabilized and of the same diameter throughout patterning.

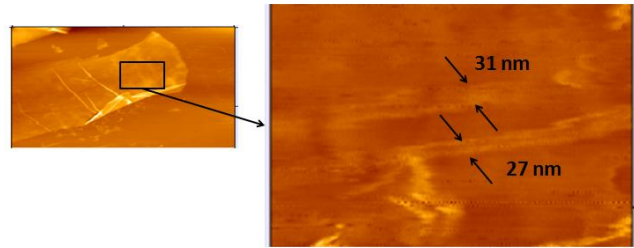


Figure 3: Experiment on few layer graphene. The minimum line width achieved was 27 nm under the following conditions: Voltage – -9.00V, setpoint – 20.0 nm, tip speed – 1.5 $\mu\text{m/s}$ (20 ms holdtime).

Based on the results achieved from experiments similar to Figure 2, we optimized the process parameters to scan 31 nm and 27 nm width lines in few layer graphene (Figure 3). The voltage threshold found for etching was -8.00V. However, this value produced unreliable results unless dot patterns were fabricated. To etch these minimal line widths, -9.00V was used at a fast tip speed of 1.5 $\mu\text{m/s}$ under high humidity conditions (>70% relative humidity) to ensure the stability of the water meniscus.

4 CONCLUSION

This experimental work on AFM tip-based oxidation lithography performed on graphite has illustrated the effect of parameter variation on the resulting feature size. Based on the results of this work, the minimum line width on few layer graphene was found to be 27 nm. Minimum graphite line width on graphite was found to be 55.0 nm. Further progress can be achieved by incorporating environmental control, which can indicate the effect of humidity on feature size. Forthcoming experiments will use this work's data to pattern single layer graphene into quantum nanoribbons for electron transport studies.

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